REMARKS

Overview of the Office Action

Claims 16-21, 23, 24, 26 and 28 have been rejected under 35 U.S.C. §102(b) as anticipated by U.S. Patent No. 6,440,810 ("Johansson").

Claims 22, 25, 27 and 29 have been rejected under 35 U.S.C. §103(a) as unpatentable over Johansson in view of U.S. Patent No. 6,028,345 ("Johnson").

Status of the claims

Claim 16 has been amended.

Claims 16-29 remain pending.

Rejection of claims 16-21, 23, 24, 26 and 28 under 35 U.S.C. §102(b)

The Office Action states that Johansson teaches all of Applicants' recited elements.

Independent claim 16 recites a method for the production of a bipolar transistor that includes a highly doped extrinsic base. Applicants' recited method includes, inter alia, "applying an implantation mask and patterning in such a way that an opening remains in a region provided for a later extrinsic base", "introducing BF_2 as a dopant of a first conductivity type into the dielectric layer after the application of the mask", and "indiffusing, in a controlled thermal step, the dopant into the semiconductor substrate from the dielectric layer, an extrinsic base doped in low-resistance fashion arising", which Johansson fails to teach or suggest.

Johansson discloses a method for forming base regions and for opening an emitter window in a silicon bipolar transistor. According to Johansson, a silicon substrate is provided with suitable device isolation. A first base region is formed in, or on top of, the substrate. A thin

layer of oxide is formed on the first base region. A layer of silicon is formed on top of the thin oxide layer. The silicon layer of Johansson is a second base region. The silicon layer is ion implanted. A layer of a dielectric is formed on top of the silicon layer. The dielectric isolates base and emitter regions of the transistor. The structure of Johansson is patterned in order to define the emitter window. The structure inside the defined emitter window area is etched and the thin oxide layer is used as etch stop, thus forming the emitter window. The structure of Johansson is subsequently heat treated to break up the oxide such that the first and second base regions contact each other.

The Examiner cites col. 4, lines 41-43 and Fig. 7 of Johansson as teaching Applicants' recited step of applying an implantation mask and patterning in such a way that an opening remains in a region provided for a later extrinsic base. Applicants disagree.

Johansson describes only a photoresist mask 13 (see Fig. 7 of Johansson). The photoresist mask of Johansson includes a central opening 15 provided for the production of the emitter window in the amorphous silicon layer (α-Si) 9 and in the oxide layer 11, which is applied on the amorphous silicon layer 9. Both layers of the photoresist mask of Johansson cover the entire surface of silicon dioxide layer 7. Johansson fails to teach or suggest a mask that includes an additional opening provided for an extrinsic base.

In contrast to Johansson, Applicants' recited invention includes an emitter window EF (see Fig. 4 of Applicants' published specification), and a separate base window. Specifically, as described in detail in paragraph [0041], "[i]n the region of the extrinsic base EB, above the base layer in the dielectric layer DS and the other layers optionally applied thereabove, in a window, the base layer BS is uncovered and the base contact BK is applied".

Therefore, Johansson fails to teach or suggest, "applying an implantation mask and

patterning in such a way that an opening remains in a region provided for a later extrinsic base", as recited in Applicants' claim 16.

The Examiner cites col. 4, lines 25-34 and Fig. 5 of Johansson as teaching Applicants' recited step of introducing BF_2 as a dopant of a first conductivity type into the dielectric layer after the application of the mask. Applicants disagree.

Johansson discloses implantation of BF2 into the whole surface of the amorphous silicon layer 9, which takes place before the production of the only photomask 13 (see Fig. 4 of Johansson). Although this implantation is provided for the production of the extrinsic base, the necessary structuring of the implanted region is effected only afterwards by a removal of the layers in the area of the emitter window.

Therefore, Johansson fails to teach or suggest "introducing BF_2 as a dopant of a first conductivity type into the dielectric layer after the application of the mask", as recited in Applicants' claim 16.

Further, Johansson discloses implantation of BF_2 into the amorphous silicon (a-Si) layer 9. According to Johansson, "[t]he energy is selected so that all of the boron ions will be contained within the α-Si" (see col. 4, lines 28-29 of Johansson). Therefore, BF_2 is expressly not intended to be implanted into the dielectric silicon dioxide layer 7, which is under the amorphous silicon. Further, the upper dielectric layer 11 is not yet present in this state of the process of Johansson.

Therefore, Johansson again fails to teach or suggest "introducing BF_2 as a dopant of a first conductivity type into the dielectric layer after the application of the mask", as recited in Applicants' claim 16.

The Examiner cites col. 4, lines 25-34 and Fig. 5 of Johansson as teaching Applicants'

recited step of indiffusing, in a controlled thermal step, the dopant into the semiconductor substrate from the dielectric layer, an extrinsic base doped in low-resistance fashion arising. Applicants disagree.

Johansson discloses that in a later thermal annealing process, which also drives dopants out of the emitter, the oxide will break up and the two silicon layers 5 and 9 will contact each other and form a thick, highly doped silicon layer for the extrinsic base (see col. 4, lines 24-34 of Johansson). The question of whether a diffusion of dopants from the upper silicon layer 9 into the lower silicon layer 5 participates in this process is not discussed. However, Johansson mentions that, apart from using an ion implantation, the silicon layer 5 can be applied already highly doped, so that a subsequent diffusion of dopant into the silicon layer 5 is not necessary (see col. 3, lines 60-63 of Johansson). Hence, diffusion from a dielectric layer into semiconductor material is not necessary and is consequently not provided.

Therefore, Johansson fails to teach or suggest "indiffusing, in a controlled thermal step, the dopant into the semiconductor substrate from the dielectric layer, an extrinsic base doped in low-resistance fashion arising", as recited in Applicants' claim 16.

In view of the foregoing, Applicants' submit that Johansson fails to teach or suggest the subject matter recited in independent claim 16. Accordingly, claim 16 is patentable over Johansson under 35 U.S.C. §102(b).

Dependent claims

Claims 17-21, 23, 24, 26, and 28, which depend from independent claim 16, incorporate all of the limitations of independent claim 16 and are, therefore, deemed to be patentably distinct over Johansson for at least those reasons discussed above with respect to independent claim 16.

Rejection of claims 22, 25, 27, and 29 under 35 U.S.C. §103(a)

The Office Action states that the combination of Johansson and Johnson teaches all of the elements recited in Applicants' claims.

Johansson has been previously discussed and fails to teach or suggest the invention recited in Applicants' independent claim 16.

Because Johansson fails to teach or suggest the subject matter recited in Applicants' independent claim 16, and because Johnson fails to teach or suggest any elements of independent claim 16 that Johansson is missing, the addition of Johnson to the reference combination fails to remedy the above-described deficiencies of Johansson.

Claim 22, 25, 27, and 29, which depend from independent claim 16, incorporate all of the limitations of independent claim 16 and are therefore deemed to be patentably distinct over Johansson and Johnson for at least those reasons discussed above for independent claim 16.

Conclusion

In view of the foregoing, reconsideration and withdrawal of all rejections, and allowance of all pending claims is respectfully solicited.

Should the Examiner have any comments, questions, suggestions, or objections, the Examiner is respectfully requested to telephone the undersigned in order to facilitate reaching a resolution of any outstanding issues.

Respectfully submitted,
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Dated: December 5, 2008